

Driver Circuit with Low Power  
Termination Mode  
Applicants: Hecht, et al.  
Docket No.: A0312.70490US01  
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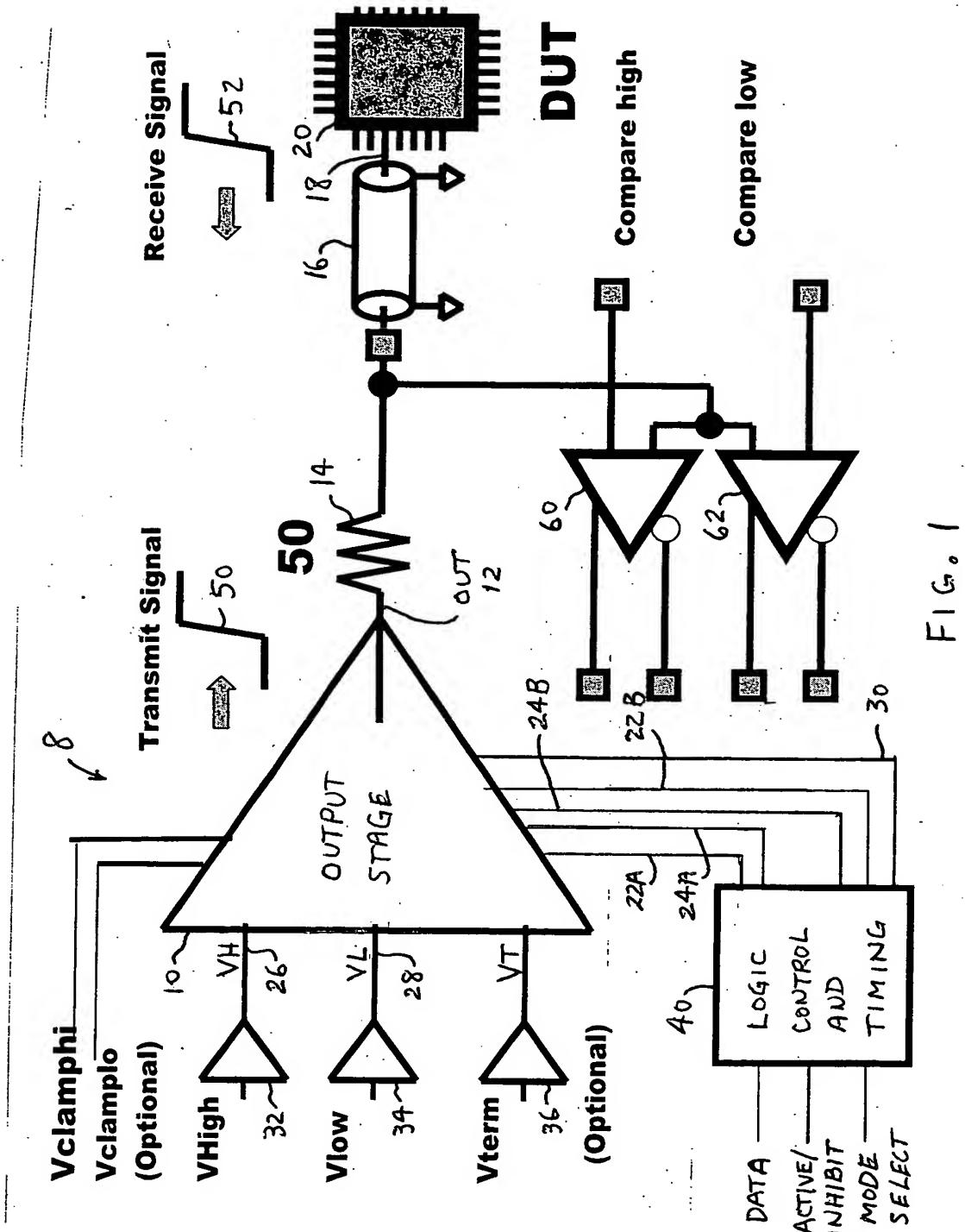


Fig. 1

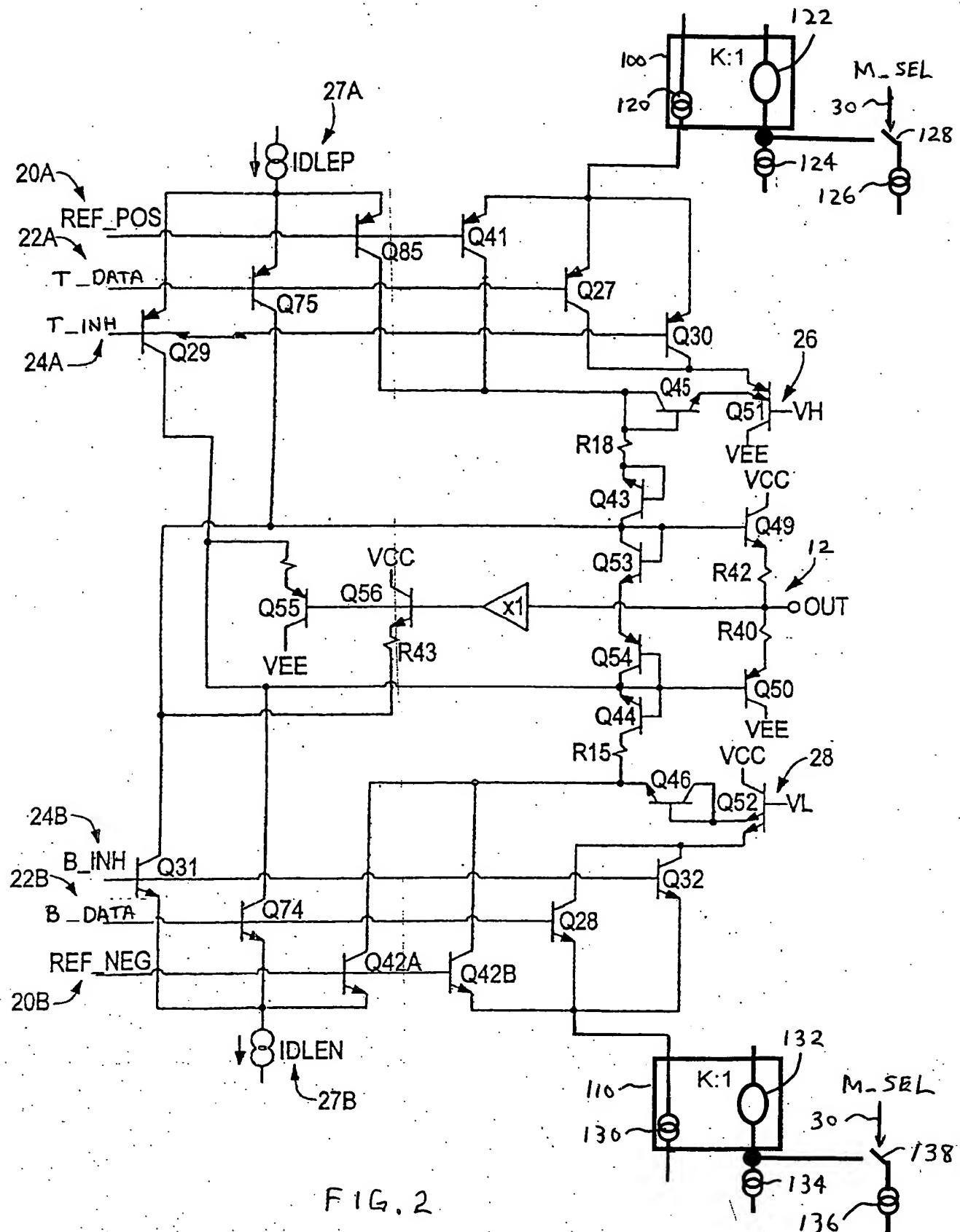


FIG. 2

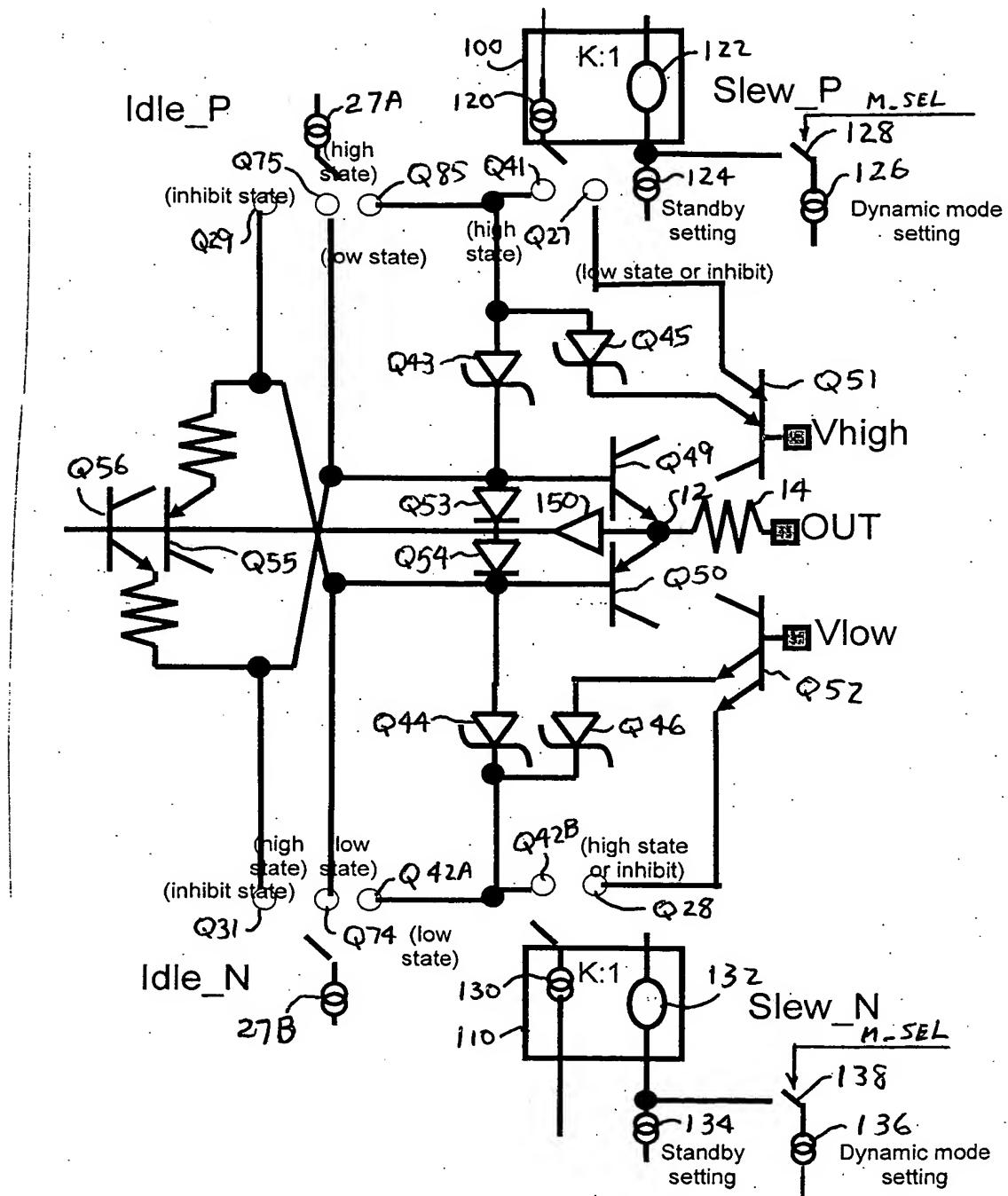


FIG. 3

DRIVER MODE	DRIVER STATE	SLEW CURRENT	HIGH-SIDE TRANSISTORS ON	LOW-SIDE TRANSISTORS ON
Dynamic	Low (ACTIVE)	Dynamic + Standby	Q75, Q27	Q42A, Q42B
Dynamic	High (ACTIVE)	Dynamic + Standby	Q85, Q41	Q74, Q28
Dynamic	INHIBIT	Dynamic + Standby	Q29, Q27	Q31, Q28
Termination	Low (ACTIVE)	Standby	Q75, Q27	Q42A, Q42B
Termination	High (ACTIVE)	Standby	Q85, Q41	Q74, Q28
Termination	INHIBIT	Standby	Q29, Q27	Q31, Q28

FIG. 4